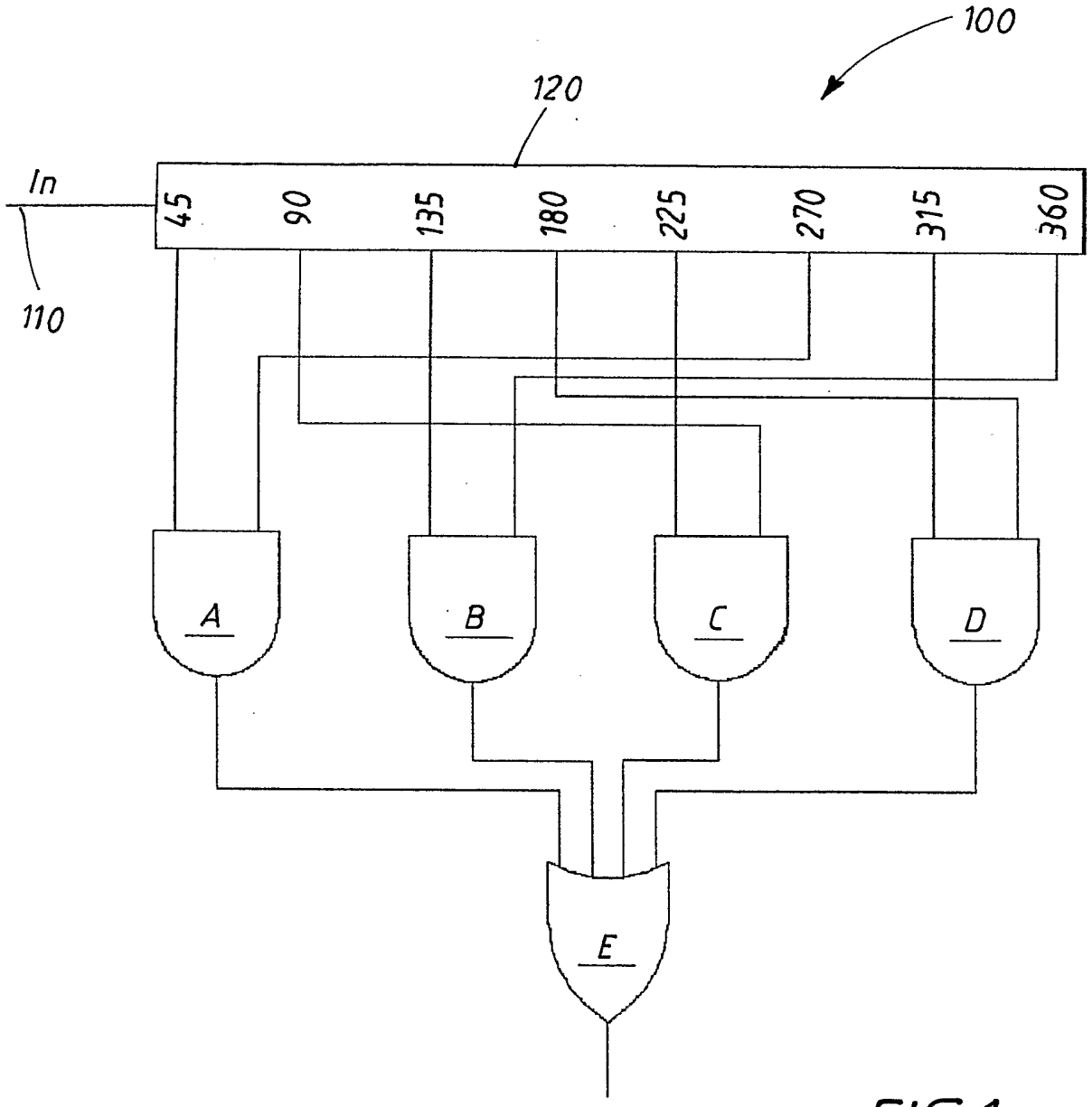

FIG.1

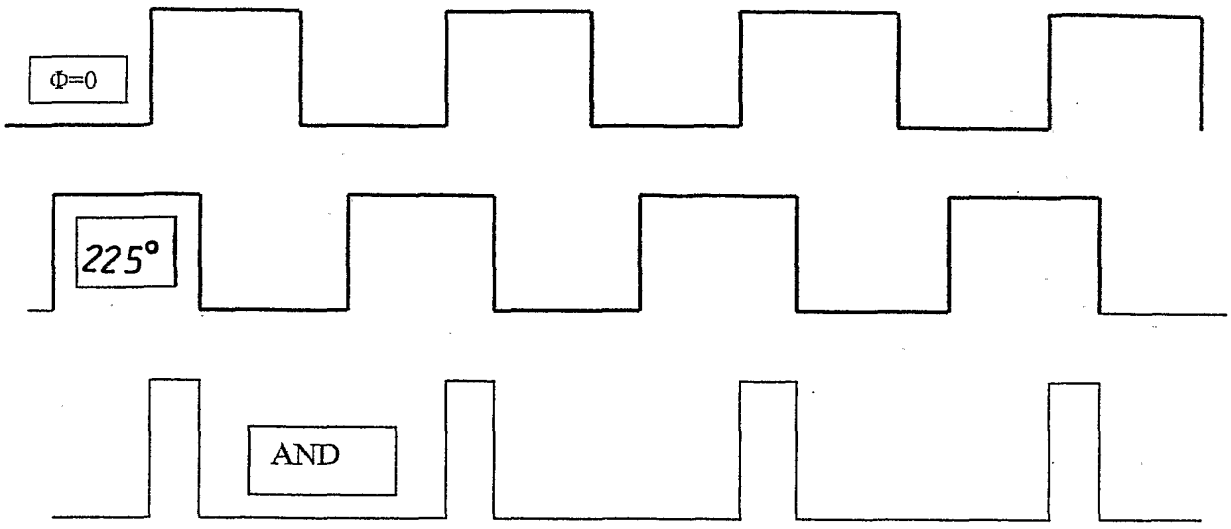

FIG.2

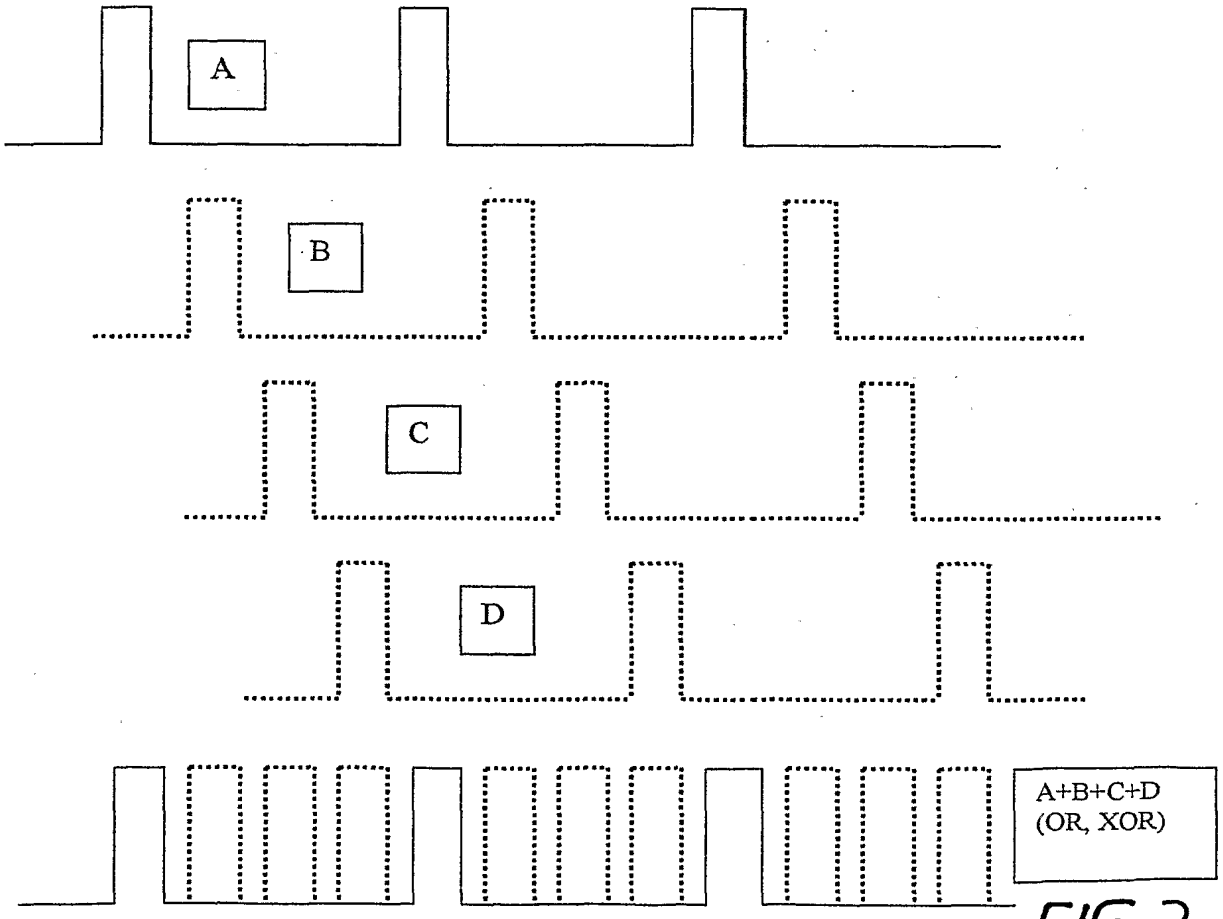

FIG.3

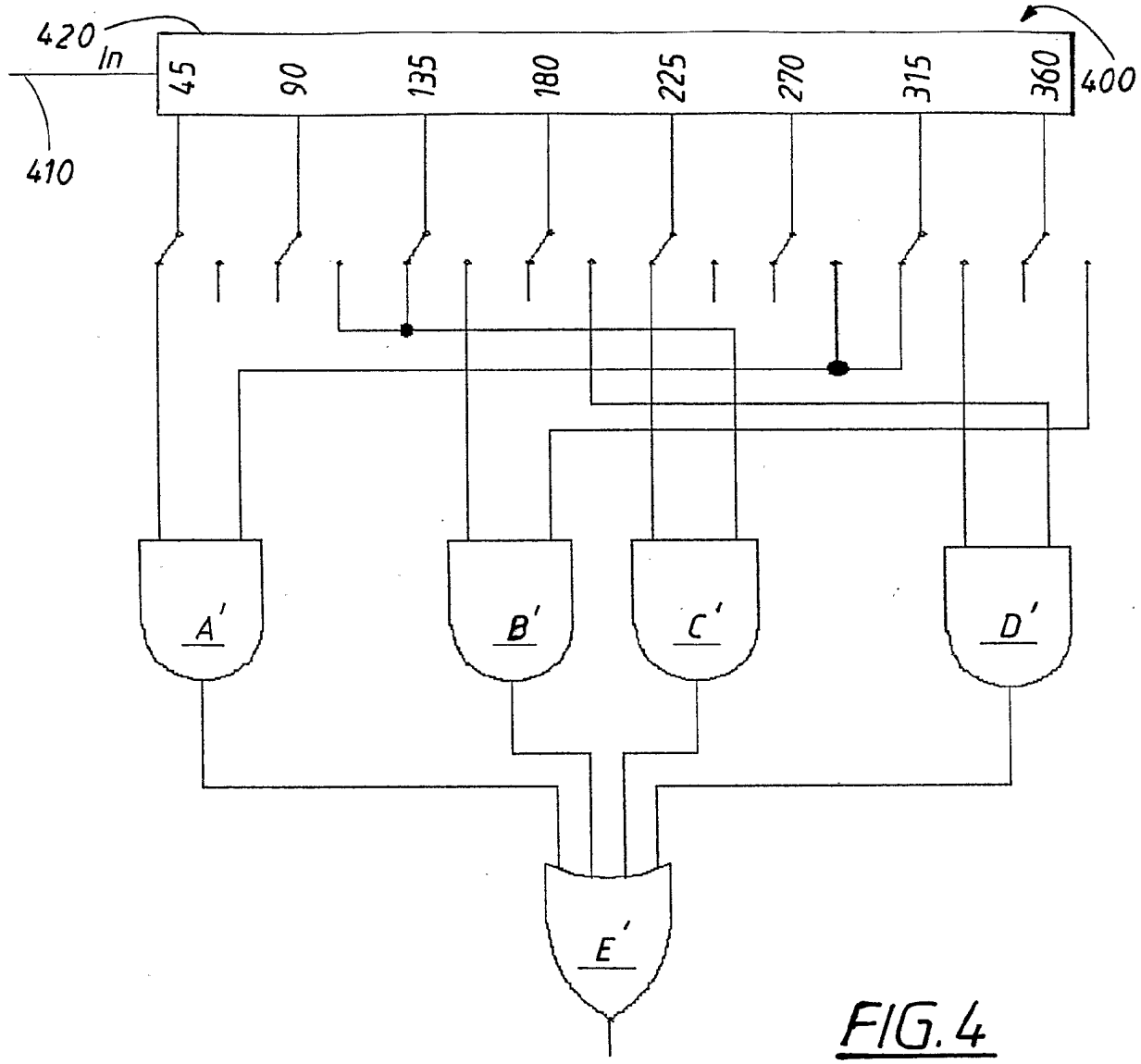
420
In
410
45
90
135
180
225
270
315
360
400
A'
B'
C'
D'
E'

FIG. 4

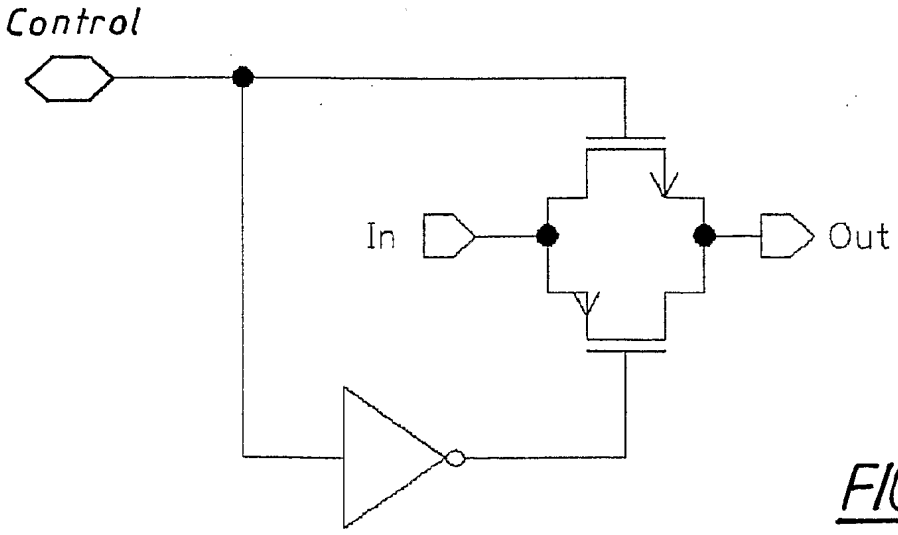
Control
In
Out

FIG. 5

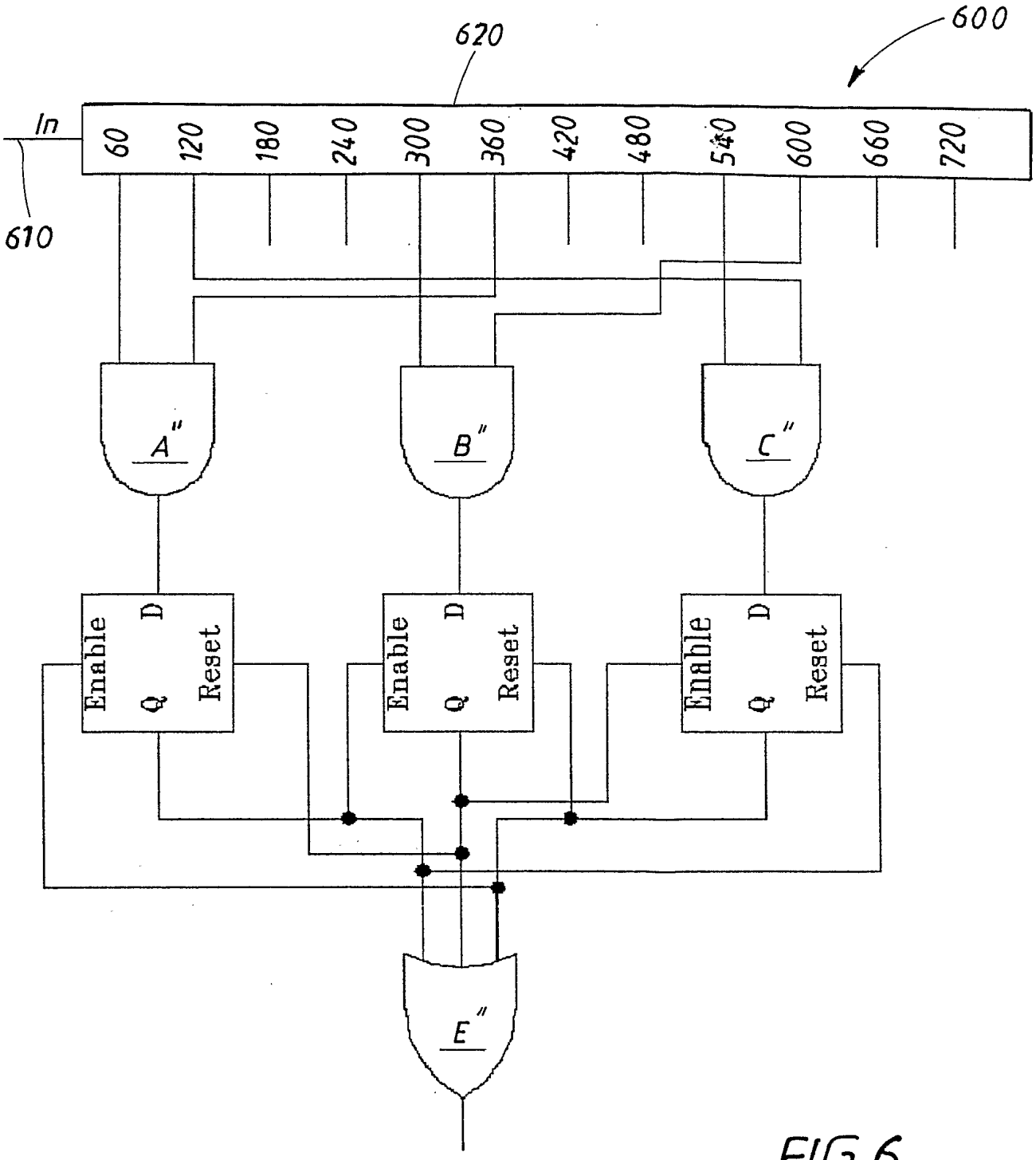
600
620
In
610
60
120
180
240
300
360
420
480
540
600
660
720
A"
B"
C"
Enable
D
Q
Reset
E"

FIG.6

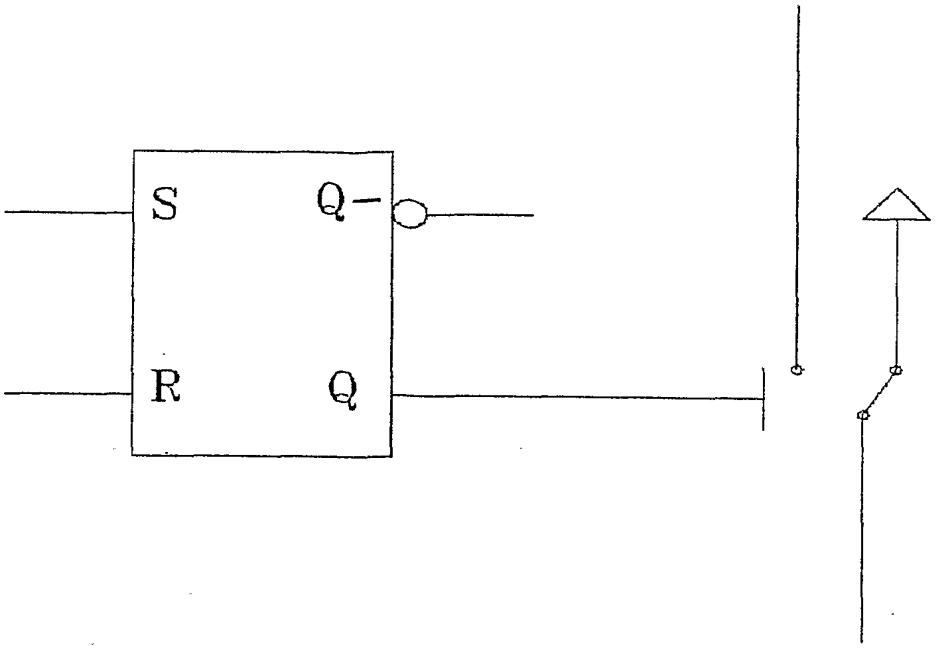

FIG.7

S	R	Q_{n+1}
0	0	Q_n
1	0	1
0	1	0
1	1	irrelevant

FIG.8

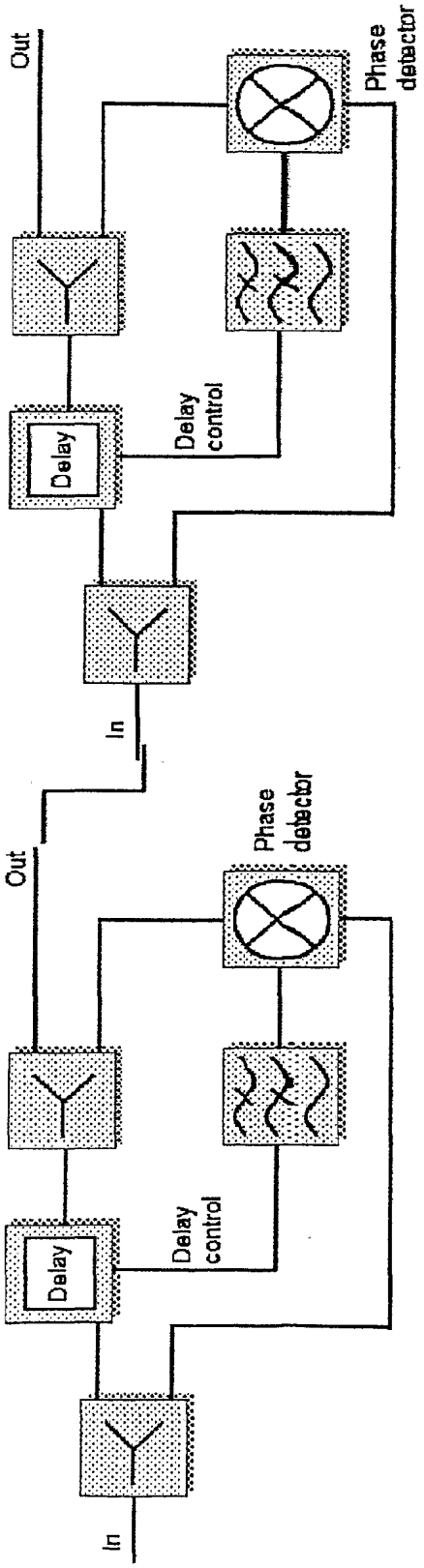
Out
Phase detector
Delay
Delay control
In
Out
Phase detector
Delay
Delay control
In

FIG.9